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Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450 Art Unit 2183

Attn: Mail Stop COC

Re:

U.S. Utility Patent

Patent No. 6,934,829 B2; Issued: August 23, 2005

High-Performance, Superscalar-Based Computer System With Out-

Of-Order Instruction Execution

Inventors:

NGUYEN et al.

Our Ref:

SP015.C16(1397.028000G)

Sir:

Certificate

Transmitted herewith for appropriate action are the following documents: DEC 2 0 2005

of Correction 1. Request for Certificate of Correction Under 37 C.F.R. § 1.322;

- 2. Certificate of Correction Form PTO/SB/44; and
- 3. One (1) return postcard.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

Sterne, Kessler, Goldstein & Fox PLLC. : 1100 New York Avenue, NW : Washington, DC 20005 : 202.371.2600 f 202.371.2540 : www.skgf.com

DEC 2 1 2005

Commissioner for Patents December 15, 2005 Page 2

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Thomas C. Fiala
Attorney for Patentees

Attorney for Patentees Registration No. 43,610

TCF/mjg Enclosures



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent of:

NGUYEN et al.

Patent. No.: 6,934,829 B2

Issued: August 23, 2005

For: High-Performance, Superscalar-Based Computer System With Out-Of-Order Instruction Execution Confirmation No.: 9218

Art Unit: 2183

Examiner: Daniel H. Pan

Atty. Docket: SP015.C16(1397.028000G)

Request for Certificate of Correction Under 37 C.F.R. § 1.322

Attn: Certificate of Correction Branch

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

It is hereby requested that a Certificate of Correction under 37 C.F.R. § 1.322 be issued for the above-captioned United States Patent. This Certificate of Correction is being requested due to mistakes which appear in the printed patent. These mistakes were made by the U.S. Patent and Trademark Office.

Specifically, the printed patent contains the following errors for which a Certificate of Correction is respectfully requested:

• Under OTHER PUBLICATIONS on page 3 of the patent, the reference to "B-Bit Embedded controller Handbook....", should appear as --8-Bit Embedded controller Handbook--. Please see the IDS submitted on October 31, 2003 for support of this correction.

• In Claim 8, column 56, line 14, "...the instruction arid a register..." should appear as --the instruction and a register--. Please see the Amendment and Reply filed December 22, 2004 for support of this correction.

Remarks

The above-noted corrections do not involve such changes in the patent as would constitute new matter or would require reexamination.

A completed Form PTO/SB/44 accompanies this request, with the above-noted corrections printed thereon. Accordingly, a Certificate of Correction is believed proper and issuance thereof is respectfully requested.

The Commissioner is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

Stepne, Kessler, Goldstein & Fox p.l.l.c.

Thomas C. Fiala Attorney for Patentees Registration No. 43,610

Date: 12 (5/05

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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

CERTIFICATE OF CORRECTION		
	C 004 000 D0	Page <u>1</u> of <u>1</u>
PATENT NO. :	6,934,829 B2	
APPLICATION NO.:	10/697,257	
ISSUE DATE :	August 23, 2005	
INVENTOR(S) :	Le Trong NGUYEN	
It is certified to is hereby corrected	hat an error appears or errors appear in the above-identified patent and the das shown below:	nat said Letters Patent
	PUBLICATIONS on page 3 of the patent, the reference to "B-Bit Enshould appear as8-Bit Embedded controller Handbook	mbedded controller
In Claim 8, colu and a register	mn 56, line 14, "the instruction arid a register" should appear a	sthe instruction

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Thomas C. Fiala

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.